ABSTRACT
In this paper, a hybrid energy storage system is devised using Electro-Chemical Double Layer Capacitors (Ultra-Capacitors) and batteries. The objective of the work is to design and analyze a hybrid energy storage system in order to achieve optimal power flow from sources considering load demand. Electro-Chemical Double Layer Capacitors are suited to supplying loads of short duration and high power, while batteries are better suited to supplying loads of long duration and low power. The loads on a domestic system are a combination of long duration low power demand and short duration high power demand. The relative strengths of batteries and super-capacitors lend themselves to supplying the two components of load. This work seeks to quantify what gains may be made by combining lead acid batteries and ultra-capacitors in a hybrid energy storage system and dispatching the two components to supply the load components suited to their strengths.

Key words: Batteries, hybrid energy storage, photovoltaic system, ultra-capacitors

INTRODUCTION
Solar energy is a promising technology in terms of supply security and sustainable environment. Nevertheless, due to its intermittent character, it has some drawbacks. In most cases, a solution consists in adding a secondary source to support the photovoltaic system. In other cases, electrochemical batteries are used as storage but to reach autonomy, it is often necessary to oversize them, and this increases the cost and the size of the over-all system.[1][2] Almost all the existing solar energy harnessing projects using PV(Photo Voltaic Cell Arrays) panels are designed to meet the constant or nearly constant loads (load of unvarying nature).

The operation of these systems under varying load environment has proved to be unsatisfactory (at peak loads the system has to draw power from the grid and when under loaded the system works inefficiently). Under such varying load circumstances the Photovoltaic System with a Battery and Ultra-capacitor Energy Storage Hybrid System has proved to be highly beneficial with a view that the ultra-capacitor has a higher power density and can provide a large amount of power over a short period of time. [3][4][5] Batteries and ultra-capacitors complement each other in terms of their power and energy densities. In the hybrid configuration the batteries supply the load demand energy; while the ultra-capacitor bank supplies the instantaneous power requirements.

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The operation is reported satisfactory for various load demand profiles including constant, peak, pulse, and domestic loads.[6][7]

II. PV ARCHITECTURE
As shown in Fig. 1, a PV system is constituted by photovoltaic panels (main source), DC/DC and DC/AC converters, control module and energy storage [8][9][10].

Components and Models
Photovoltaic panels:
Photovoltaic panels consist of several modules; modules are composed by cells which are in series in order to obtain adequate tension. Photovoltaic cells transform luminous energy (solar) into electrical energy. The equivalent circuit of a photovoltaic cell is shown in Fig. 2. It consists of an ideal source producing a current \( I_{ph} \), proportional to incident light, in parallel with a diode D. Shunt resistance \( R_p \) models the effect of leak current but in many cases this can be neglected due to its relative large value. [11]

\[
I = I_{ph} - I_D \quad \text{and} \quad I_D = I_S \left( \exp \left( \frac{V_D}{V_T} \right) - 1 \right)
\]

Where
\( I_{ph} = I_{ph,T1}(1 + K_0(T - T_1)) \)
\( I_p = \frac{V_D}{R_p} = \frac{V + I_R}{R_p} \)

Finally:
\[
I = I_{ph} - I_S \left( \exp \left( \frac{V - I_R}{mV_T} \right) - 1 \right) - \frac{V + I_R}{R_p}
\]

Photovoltaic output power is intermittent and dependent on climatic conditions. High starting currents are required by some applications, such as starter motor. The startup current of a starter motor can be 6-10 times greater than the nominal current. Although these large current spikes only last for a short duration, the traditional Lead Acid battery must be sized to supply this current, resulting in increased
battery storage. The ultra-capacitor has a higher power density than the battery. It can provide a large amount of power over a short period of time. Conversely, the battery has a higher energy density compared to the ultra-capacitor, which can be utilized for the off-peak periods.

III. ULTRA-CAPACITOR MODULE

Ultra or Super-capacitors are efficient energy storage devices that have been developed in recent years into products that can be used in electrical power applications for energy storage. Renewable energy systems by their intermittent nature are dependent on energy storage and/or traditional fossil fuel generation to maintain reliable electricity supply. Super-capacitors are suited to supplying loads of short duration and high power, while batteries are better suited to supplying loads of long duration and low power. The loads on the existing system are a combination of long duration low power demand and short duration high power spikes in demand. The relative strengths of batteries and super-capacitors lend themselves to supplying the two components of load. This project seeks to quantify what gains may be made by combining lead acid batteries and super-capacitors in a hybrid energy storage system and dispatching the two components to supply the load components suited to their strengths. [14][15]

Porous material used on the electrode of EDLCs exhibit non-ideal behavior which causes the capacitance and resistance to be distributed which can be represented by the figure. 3 that follows, for representing the real electrical response.

Advantages:

- **High energy storage.** Compared to conventional capacitor technologies, EDLCs possesses orders of magnitude higher energy density. This is a result of using a porous activated carbon electrode to achieve a high surface area.
- **Low Equivalent Series Resistance (ESR).** Compared to batteries, Ultra-Capacitors have a low internal resistance, hence providing high power density capability.
- **Low Temperature performance.** Ultra-Capacitors, are capable of delivering energy down to -40°C with minimal effect on efficiency.
- **Fast charge/discharge.** Since EDLCs achieve charging and discharging through the absorption and release of ions and coupled with its low ESR, high current charging and discharging is achievable without any damage to the parts.

![Fig.3. Distributed ELDC](image)

![Fig.4. Equivalent Circuit](image)

Battery=Ideal Voltage Source, $V_b$ in series with internal resistance, $R_b$

Ultra-Capacitor= Capacitor, $C_c$ in series with an equivalent resistance, $R_c$

![Fig.4. Voltage Wave Form of Hybrid System](image)
Battery Current,
\[ i_b(t) = \frac{1}{R_b}[V_b - V_0(t)] \]

Ultra Capacitor Current,
\[ i_c(t) = i_0(t) - i_b(t) \]

IV. PERFORMANCE PARAMETERS

1. Temperature effects and performance changes over time:
The performance of ultra-capacitors is very stable over a wide operating temperature due to the chemistry and physical make up of the products. An advantage of the ultra-capacitors’ organic based electrolyte is its low freezing point. This enables the ultra-capacitors to be utilized over a wide range of temperatures, with relatively unaffected performance.

2. Lifetime
Ultra-capacitor life is predominantly affected by a combination of operating voltage and operating temperature. The ultra-capacitor has an unlimited shelf life when stored in a discharged state. When referring to ultra-capacitor life the data sheets reflect the change in performance, typically decrease in capacitance and increase in resistance. The ultra-capacitor does not experience a true end of life rather the performance continually degrades over the life of the use of the product.

The typical degradation behavior of the ultra-capacitor resembles that of an exponential decay. The majority of the performance change occurs during the initial use of the ultra-capacitor and this performance change then levels off over time. The most dramatic effect of the life degradation is on the internal resistance of the device.

In many applications, such as UPS, the ultra-capacitors (UC) will be maintained at working voltage until needed. To give one example, a 15% reduction in rated capacitance and a 40% increase in rated resistance may occur for an ultra-capacitor held at 2.5 V after 88,000 hrs at 25˚C. The influence of temperature has a doubling effect for every 10˚C, this behavior of UC can be used to predict the expected performance change for a variety of conditions.

3. Cycling
From cycle testing performed on ELDCs, under typical conditions the product is expected to provide in excess of 1 million duty cycles with an approximate 20% reduction in rated capacitance.

4. Frequency response
Ultra-capacitors have a typical time constant of approximately one second. One time constant reflects
the time necessary to charge a capacitor 63.2% of full charge or discharge to 36.8% of full charge. The time constant of an ultra-capacitor is much higher than that of an electrolytic capacitor. Therefore, it is not possible to expose ultra-capacitors to a continuous ripple current as overheating may result. The ultra-capacitor can respond to short pulse power demands, but due to the time constant the efficiency or available energy is reduced. [19]

5. Voltage
Ultra-capacitors are capable of operating between their rated voltage and zero volts. Occasional spikes above the rated voltage will not immediately affect the capacitor. Depending on the frequency and duration of voltage spikes the life will be reduced. Efficient utilization of the available energy and power storage is achieved with the widest operating voltage range used. Most electronics have a minimum voltage threshold for utilization, limiting the effective utilization voltage of the capacitor although there is no limitation in the capacitor itself. Since the energy in the capacitor is proportional to the voltage squared according to $E = \frac{1}{2} CV^2$.

It is possible to utilize approximately 75% of the available energy if the application utilizes from the rated voltage to $\frac{1}{2}$ rated voltage of the capacitor.

6. Polarity
Unlike many batteries the anode and cathode of an ultra-capacitor are comprised of the same material. If the positive and negative terminal and casing are also comprised of similar materials, then theoretically the ultra-capacitor has no true polarity. For manufacturing and consistency purposes the terminals are marked with polarity. It is recommended practice to maintain the polarity although catastrophic failure will not occur if the ultra-capacitor is reversed charged some reason. If the ultra-capacitor has been conditioned for charge in a certain direction and then is changed, the life can be reduced due to this conditioning. Due to the corrosion potential it is required to maintain the polarity indicated on the UC, and reverse polarity will cause accelerated life reduction.

7. Charging
Since the energy storage mechanism of the ultra-capacitor is not a chemical reaction, charging /discharging of the ultra-capacitors can occur at the same rate. Therefore, the rated current for the ultra-capacitor applies for both charge and discharge. The efficiency of charge and discharge are in practical terms the same. A variety of methods are possible for charging of the ultra-capacitors. This may be either through constant current or constant power charging via a dc source or through ac charging methods.

8. Series connection and balancing
Since the individual ultra-capacitor cell voltage is relatively limited compared to the majority of application requirements, it is necessary to series connect the ultra-capacitors to achieve the voltage required. Because each ultra-capacitor will have a slight tolerance in capacitance and resistance it is necessary to balance, or prevent, individual ultra-capacitors from exceeding its rated voltage. Balancing can be achieved through two different methods, active balancing or passive balancing:

Active balancing schemes are varied. This methodology always attempt to balance two adjoining ultra-capacitors based on the voltage mismatch between the two. The maximum current during balancing varies by capacitor to capacitor. Passive balancing implies no variation in the voltage regulation as a function of the ultra-capacitor condition. The most typical method of passive balancing utilizes resistors in parallel with the ultra-capacitors.

A variety of interconnection methods are practiced. They range from bus bar interconnecting to soldering. In general the larger the cell capacitance the more critical the cell interconnection becomes. The larger capacitance devices have internal resistances on the order of a few hundred micro-ohms. A poor interconnection can have more resistance than the internal resistance of the device itself. Larger devices will generally be required to carry larger currents, thus necessitating reliable interconnects.

9. Efficiency
Unlike batteries, the ultra-capacitor has the same efficiency during charge or discharge. This enables the ultra-capacitor to be recharged quickly without current limiting as long as the current is within the rated current for the device.

The only efficiency losses associated with ultra-capacitors are due to internal resistance of the device
resulting in IR drop during cycling. For most uses the ultra-capacitor efficiency is in excess of 98%. For high current or power pulsation the efficiency is reduced. Typical efficiency under high current pulses is still greater than 90%.

10. Thermal Properties
For minimum performance influence over the life of the application it is necessary to maintain the ultra-capacitor core temperature within the rated temperature range of the device. The lower the temperature is maintained the better for life considerations. For this reason, since all current passes through the capacitor terminals, cooling at the capacitor ends or terminals is the most efficient means for cooling of the capacitor. Depending on the duty cycle of the application, cooling can be accomplished via heat sinks (conduction), air flow (convection) or a combination of the two. Consideration should be made for the duty cycle and resulting capacitor temperature as well as the anticipated ambient temperature the device will be operating under. The combination of the two should not exceed the operating temperature for the ultra-capacitor.

CONCLUSION
From the discussion above it could be concluded that adding ultra-capacitors decreases the losses in batteries. This advantage is all the more important at low temperatures. It is thus possible to reduce the dimensioning of the batteries, especially when the influence of the temperature is taken into account (batteries having poor yield at low temperature). Another effect not shown here is that ultra-capacitors should improve the life-cycles and sustainability of batteries by limiting their discharge current. However, the fact that ultra-capacitors cannot store a lot of energy means that they cannot be used without batteries. Finally, a more effective charge and discharge protocol not based only on voltages but also on the storage state of charge should improve the efficiency of the whole system.

REFERENCES